

METHODS AND APPARATUS FOR PERFORMING PARALLEL INTEGER MULTIPLY ACCUMULATE OPERATIONS

ABSTRACT OF THE DISCLOSURE

According to the invention, a processing core that executes a parallel multiply
5 accumulate operation is disclosed. Included in the processing core are a first, second and
third input operand registers; a number of functional blocks; and, an output operand register.
The first, second and third input operand registers respectively include a number of first input
operands, a number of second input operands and a number of third input operands. Each of
the number of functional blocks performs a multiply accumulate operation. The output
10 operand register includes a number of output operands. Each of the number of output
operands is related to one of the number of first input operands, one of the number of second
input operands and one of the number of third input operands.

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